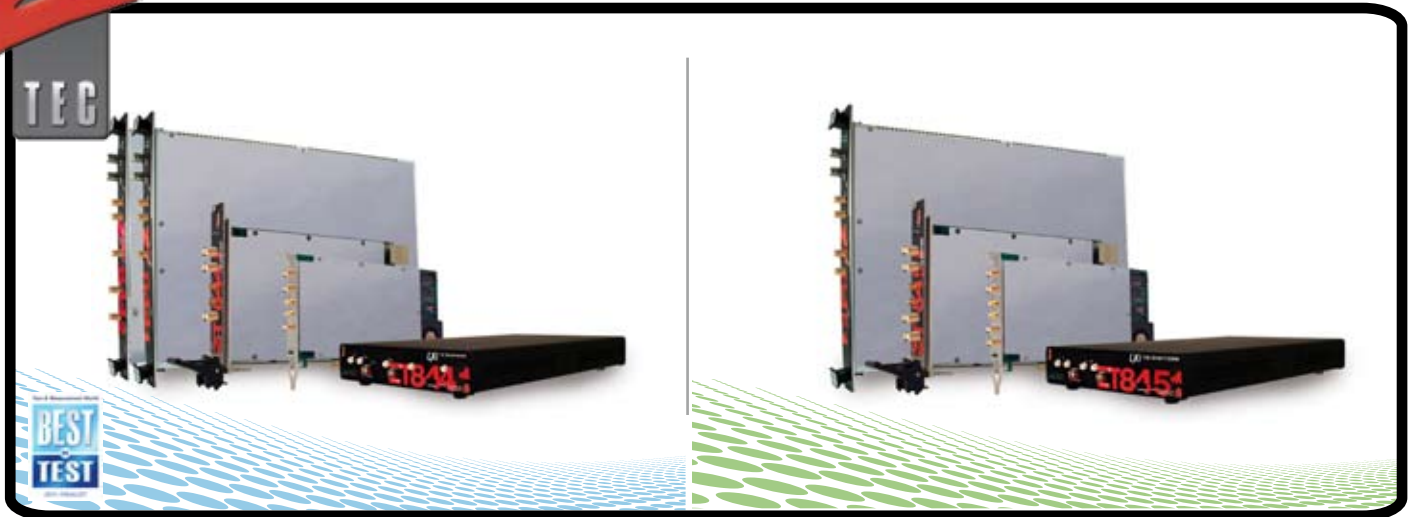


Which 84XX Series works for you?



Series	Frequency Range	Instantaneous Bandwidth	Real-Time Sig. Processing	I/Q Data	ADC	Spurious Free Dynamic Range	Memory	Inputs or Channels	Input Voltage Ranges
ZT8440	DC to 1 GHz	100 Hz to 160 MHz	100 Hz to 160 MHz FPGA based DDC w/ fractional resampling	16 bit resolution	Dual 400 MS/s 14 bit	>80 dBc	512 MB	2 single inputs	fixed +10dBm
ZT8450	DC to 300 MHz	100 Hz to 160 MHz	100 Hz to 160 MHz FPGA based DDC w/ fractional resampling	16 bit resolution	Dual 400 MS/s 14 bit	>80 dBc	512 MB	2 Diff Inputs	+10dBm, 0dB, -10dBm, -20dBm

ZT8440 RF/IF Digitizer

Product Specifications:

- Best suited as an IF digitizer for use with an RF Downconverter
- A downconverter's precision clock can be applied as external ADC sampling clock input on the ZT8440 IF digitizer for optimum phase noise performance
- Single-ended +10dBm input signal range
- DC to 1 GHz frequency range
- Ultra-wide 160MHz instantaneous bandwidth max
- Dual 400 MS/s 14-bit ADC
- 100 S/s to 400 MS/s alias-free sample rate
- PXIe, PXI, VXI, LXI and PCI platforms
- Vector signal analysis supports OFDM (Orthogonal Frequency-Division Multiplexing) waveforms including 802.11a/n/ac, WiMAX and LTE
- Real-time Quadrature Digital Down Converter (DDC) with fractional resampling and decimation
- Complex (I/Q), real (I or Q) and simultaneous (I and Q) input signal processing configurations
- Fast all-digital frequency switching

ZT8450 I/Q Digitizer

Product Specifications:

- Optimized for stand-alone differential I/Q testing of baseband RFIC components (e.g. I/Q demodulators)
- High-quality internal ADC clock provides extremely low phase noise
- Differential or single-ended inputs supported
- Four I/Q input ranges with up to 30 dB gain to amplify very small I/Q signals while maintaining good noise figure (+10 dBm to -20 dBm)
- DC to 300 MHz frequency range
- Ultra-wide 160MHz instantaneous bandwidth max
- Dual 400 MS/s 14-bit ADC
- 100 S/s to 400 MS/s alias-free sample rate
- PXIe, PXI, VXI, LXI and PCI platforms
- Vector signal analysis supports OFDM (Orthogonal Frequency-Division Multiplexing) waveforms including 802.11a/n/ac, WiMAX and LTE
- Real-time Quadrature Digital Down Converter (DDC) with fractional resampling and decimation
- Complex (I/Q), real (I or Q) and simultaneous (I and Q) input signal processing configurations
- Fast all-digital frequency switching